

#510
02/US3480D1
10/30/00
Harrison

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : H. Ohtani Art Unit : 2816
Serial No.: 09/389,393 Examiner : B. William Baumeister
Filed : September 3, 1999
Title : SEMICONDUCTOR DEVICE HAVING INSULATED GATE ELECTRODE

Commissioner for Patents
Washington, D.C. 20231

October 16, 2000
[Note: October 14, 2000
was a Saturday]



AMENDMENT

Sir:

In response to the Official Action of June 14, 2000, Paper No. 3, in the above-referenced application, please amend ~~RECE~~ above-identified application as follows. OCT

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In the Claims:

Please cancel claims 4, 9, 14, and 19 without prejudice.

Please amend the claims as follows.

1. (Amended) A semiconductor device comprising:
a semiconductor layer having at least a channel region formed on
an insulating surface;

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

10/23/2000 CCHAU1 00000108 09389393

02 FC:115
03 FC:103
04 FC:102

110.00 OP
72.00 OP
160.00 OP

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

October 16, 2000

Date of Deposit

Susan Regan
Susan Regan

Typed or Printed Name of Person Signing Certificate



Attorney's Docket No.: 07977/204002/US3480D1

GP 2816
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UNITED STATES PATENT AND TRADEMARK OFFICE

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TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges
not covered, or any credits, to Deposit Account No. 06-1000.

Total Claims 24 - 20 = 4 \$72
Independent 6 - 4 = 2 \$160
Applicant hereby petitions under 37 C.F.R. \$1.136
for a 1 month extension of time. \$110

TOTAL FEE DUE \$342

A check for \$342 is attached.

Respectfully submitted,

Date: 10/16/00



Scott C. Harris
Reg. No. 32,030

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